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1966/9365  
1970-1971

	Class      Subclass	ISSUE CLASSIFICATION

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

O.I.P.E.	PATENT DATE
<p><i>KS</i> SCANNED <u>HC 2</u> Q.A. <u>AA</u></p>	

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/938528	F	438	424	2812	KOT D L-41

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## Method for manufacturing a semiconductor device

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PTO-2040  
12/89

## **ISSUING CLASSIFICATION**

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____				<b>ISSUE FEE</b>	
	_____ (Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal ___ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	

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